

## **Amendments to the Specification**

Kindly amend paragraph [0026] as follows:

**[0026]** Comparison logic 610 compares the input address to directory and autonomous error recovery unit 520 to the address 606 currently stored in storage compartment 605. If the stored and requested addresses are not equal, AND logic gates 620, 625, and 630 are not enabled. Alternatively, if the stored and requested addresses are equal, AND logic gates 620, 625, and 630 are enabled, and the response of directory and autonomous error recovery unit 520 to the request depends on the results of testing the data-valid bit and parity of the control information field of storage compartment 605. In this example the control information field comprises the data-valid bit (V), and one parity bit (P).

Kindly amend paragraph [0027] as follows:

**[0027]** If a check of the parity of the control information field 608 by parity-check logic 615 indicates that no error has been detected, the YES output of parity-check logic 615 is a logical 1. In this event, if data-valid bit V 607 is also a logical 1, AND logic gate 620 is activated so the SEL\_B signal is a logical 1. This also activates OR logic gate 640 so that the memory device handles the request as a “hit” in control logic 650.